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INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference AZ00-0176-WO	FOR FURTHER ACTION	see Notification of Transmittal of International Search Report (Form PCT/ISA/220) as well as, where applicable, item 5 below.
International application No. PCT/KR 00/01439	International filing date (day/month/year) 12 December 2000 (12.12.2000)	(Earliest) Priority Date (day/month/year) 19 July 2000 (19.07.2000)
Applicant LG ELECTRONICS INC.		

This international search report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This international search report consists of a total of 3 sheets.

It is also accompanied by a copy of each prior art document cited in this report.

1. Basis of the report
 - a. With regard to the language, the international search was carried out on the basis of the international application in the language in which it was filed, unless otherwise indicated under this item.

the international search was carried out on the basis of a translation of the international application furnished to this Authority (Rule 23.1(b)).
 - b. With regard to any nucleotide and/or amino acid sequence disclosed in the international application, the international search was carried out on the basis of the sequence listing:

contained in the international application in written form.

filed together with the international application in computer readable form.

furnished subsequently to this Authority in written form.

furnished subsequently to this Authority in computer readable form.

the statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.

the statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.
2. Certain claims were found unsearchable (See Box I).
3. Unity of invention is lacking (See Box II).
4. With regard to the title,

the text is approved as submitted by the applicant.

the text has been established by this Authority to read as follows:
5. With regard to the abstract,

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the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box III. The applicant may, within one month from the date of mailing of this international search report, submit comments to this Authority.
6. The figure of the drawings to be published with the abstract is Figure No.: 4

as suggested by the applicant.

because the applicant failed to suggest a figure.

because this figure better characterizes the invention.

None of the figures.

INTERNATIONAL SEARCH REPORT

International Application No.
PCT/KR 001439

CLASSIFICATION OF SUBJECT MATTER

IPC⁷: G01N 27/18

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁷: G01N 27/18

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0376721 A2 (SHARP) 4 July 1990 (04.07.90) the whole document.	1-4,7-11,14-17
Y		18-20
Y	EP 021225 A1 (SHIBAURA DENSHI) 7 January 1981 (07.01.81) fig. 2.	18-20

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:

..A“ document defining the general state of the art which is not considered to be of particular relevance

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..P“ document published prior to the international filing date but later than the priority date claimed

..T“ later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

..X“ document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

..Y“ document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

..&“ document member of the same patent family

Date of the actual completion of the international search 14 March 2001 (14.03.2001)	Date of mailing of the international search report 11 April 2001 (11.04.2001)
Name and mailing address of the ISA/AT Austrian Patent Office Kohlmarkt 8-10; A-1014 Vienna Facsimile No. 1/53424/535	Authorized officer NARDAI Telephone No. 1/53424/347

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/FR 00/01439

Patent document cited in search report			Publication date	Patent family member(s)			Publication date
EP A1	21225		07-01-1981	CA	A1	1149635	12-07-1983
EP A1	21225		07-01-1981	JP	A2	56000648	07-01-1981
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				US	A	5048336	17-09-1991
				JP	A2	3092754	17-04-1991
				JP	B4	6100568	12-12-1994

(19) World Intellectual Property Organization
International Bureau(43) International Publication Date
24 January 2002 (24.01.2002)

PCT

(10) International Publication Number
WO 02/06808 A1

(51) International Patent Classification⁷: G01N 27/18 (74) Agents: KIM, Yong, In et al.; Yo Sam Building, 15th Floor, 648-23 Yeoksam-dong, Kangnam-ku, Seoul 135-080 (KR).

(21) International Application Number: PCT/KR00/01439 (81) Designated States (national): AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

(22) International Filing Date: 12 December 2000 (12.12.2000) (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data: 2000/41374 19 July 2000 (19.07.2000) KR

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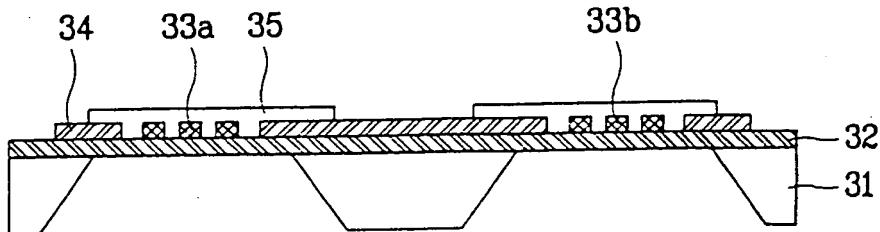
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Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ABSOLUTE HUMIDITY SENSOR



WO 02/06808 A1

(57) Abstract: An absolute humidity sensor for a microwave oven is disclosed. The absolute humidity sensor includes a substrate having a first hole and a second hole in a predetermined region, a membrane formed on the substrate, a humidity sensing element formed on the membrane where the first hole is formed, for detecting humidity exposed to the air, having a variable resistance value depending on the detected humidity, and a temperature compensating element formed on the membrane where the second hole is formed, for compensating for the resistance value of the humidity sensing element. For package, the absolute humidity sensor further includes a stem joined with a lower portion of the substrate, having pins for electrically connecting with the outside, and a hole to pass through external humidity, a wire for electrically connecting the electrode pads of the humidity sensing element and the temperature compensating element with the pins of the stem, and a metal shield case formed on an upper portion of the stem to cover an entire surface of the stem including the humidity sensing element and the temperature compensating element.

ABSOLUTE HUMIDITY SENSOR

Technical Field

The present invention relates to an absolute humidity sensor, and more particularly, to an absolute humidity sensor for a microwave oven.

Background Art

Generally, a humidity sensor is used for various purposes, for example, in a hygrometer, a humidity sensor for cooking of food in a microwave oven, and the like. Examples of currently used humidity sensors include a capacitance type humidity sensor, a relative humidity sensor, and an absolute humidity sensor. The capacitance type humidity sensor is based on variation of dielectric constants by hygroscopic property of an organic material such as polyimide. The relative humidity sensor is based on resistance variation of a semiconductor ceramic such as $MgCr_2O_4$. The absolute humidity sensor is based on a ceramic thermistor.

Of the humidity sensors, the absolute humidity sensor based on two thermistors is widely used as a humidity sensor for cooking of food in a microwave oven.

The absolute humidity sensor has an advantage in that it can stably detect the humidity because it is not susceptible to variation of a peripheral temperature.

The principles of humidity sensing of the absolute humidity sensor in the microwave oven are based on resistance variation by temperature variation of a thermistor as water vapor generated from food during cooking of food absorbs heat of the thermistor.

Fig. 1 shows a structure of a background art absolute humidity sensor. Referring to Fig. 1, two ceramic thermistors 1 and 2 coated with a passivation film such as a glass film are floating by being connected to a support pin 4 by a precious metal conductor 3 such as platinum. The ceramic thermistors 1 and 2 are packaged by a metal shield case 5 that isolates the two thermistors 1 and 2 from each other.

The thermistor 1 is exposed to the air to allow water vapor to be in contact with a surface of the thermistor 1 by means of a fine hole of the metal shield case 5. The thermistor 1 is used as a sensing element. The other thermistor 2 is sealed in a dry N_2 by the metal shield case 5 so as not to be in contact with the water vapor. The thermistor 2 is used as a reference element.

Therefore, if a bridge circuit consists of the two thermistors 1 and 2 and an external resistor, the water vapor generated from food during cooking of food absorbs heat of the thermistor 1 exposed to the air. Thus, resistance variation occurs in only the exposed thermistor 1. In this case, output variation occurs due to a bias voltage, thereby detecting the humidity.

Since the background art humidity sensor uses an element as

a ceramic thermistor, heat capacity is great and thus sensitivity is low. Also, response time is slow and the size of the sensor becomes greater.

Furthermore, the thermistor element is floating using the conductor 3 and the support pin 4 as shown in Fig. 1, and the conductor 3 and the pin 4 are spot-welded. For assembly, the reference element 2 should be sealed in a dry N_2 . For this reason, the fabrication process steps are complicate and the number of the process steps increases. Also, the cost is expensive and mass production is disadvantageous.

Disclosure of the Invention

Accordingly, the present invention is directed to an absolute humidity sensor that substantially obviates one or more of the problems due to limitations and disadvantages of the background art.

An object of the present invention is to provide an absolute humidity sensor having an improved sensitivity and high response time.

Another object of the present invention is to provide an absolute humidity sensor having a compact size and simple process steps to facilitate mass production.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of

the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described, an absolute humidity sensor according to the present invention includes a substrate having a cavity, a membrane formed on the substrate, a resistor formed on the membrane, electrode pads formed on the membrane, for electrically connecting with the resistor, and a passivation film formed on an entire surface of the resistor to cover the resistor.

The membrane is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , and $\text{SiO}_2/\text{Si}_3\text{N}_4/\text{SiO}_2$. The resistor is formed of one or more of Ti, Pt, Ni, Ni-Cr, and VO_2 . The passivation film is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , phosphor silicate glass (PSG), and polyimide.

A thermal conductive film may further be formed on a region of the passivation film where the resistor is formed. At this time, the thermal conductive film is formed on any one of Al and Au.

In another aspect, an absolute humidity sensor according to the present invention includes a substrate having a first cavity and a second cavity in a predetermined region, a membrane formed on the substrate, a humidity sensing element formed on the membrane where the first cavity is formed, for detecting humidity

exposed to the air, having a variable resistance value depending on the detected humidity, and a temperature compensating element formed on the membrane where the second cavity is formed, for compensating for the resistance value of the humidity sensing element.

The humidity sensing element and the temperature compensating element include a resistor formed on the membrane, electrode pads formed on the membrane, for electrically connecting with the resistor, and a passivation film formed on an entire surface of the resistor to cover the resistor.

A thermal conductive film may further be formed on a region of the passivation film where the resistor is formed. At this time, the thermal conductive film is formed on any one of Al and Au.

A cap may further be formed over the humidity sensing element and the temperature compensating element to cover the entire surfaces of the humidity sensing element and the temperature compensating element, and separates the humidity sensing element and the temperature compensating element from each other and seals them. A shielding film is formed in a central region of the cap to separate and seal the humidity sensing element and the temperature compensating element. A hole is formed in the cap, where the humidity sensing element is formed, to pass through external humidity. The cap is made of silicon.

The absolute humidity sensor according to the present invention further includes a stem joined with a lower portion of the substrate, having pins for electrically connecting with the outside, a wire for electrically connecting the electrode pads of the humidity sensing element and the temperature compensating element with the pins of the stem, and a metal shield case formed on an upper portion of the stem to cover an entire surface of the stem including the humidity sensing element and the temperature compensating element.

A hole is formed in a region of the stem, where the humidity sensing element is formed, or the metal shield case to pass through external humidity.

In the preferred embodiment of the present invention, an absolute humidity sensor having an improved sensitivity to humidity is fabricated using the resistor, and at the same time the sensor is integrated in one chip using a silicon micro-machining process to facilitate its mass production.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

Brief Description of the Drawings

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in

and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

In the drawings:

Fig. 1 is a structural sectional view showing a background art of an absolute sensor;

Fig. 2 is a structural sectional view showing a membrane type absolute humidity sensor according to the first embodiment of the present invention;

Fig. 3 is a structural sectional view showing a package of the membrane type absolute humidity sensor according to the first embodiment of the present invention;

Fig. 4 is a structural sectional view showing a membrane type absolute humidity sensor according to the second embodiment of the present invention;

Fig. 5 is a structural sectional view showing a package of the membrane type absolute humidity sensor according to the second embodiment of the present invention;

Figs. 6a and 6b are structural perspective views showing the package of the membrane type absolute humidity sensor according to the second embodiment of the present invention;

Fig. 7 is a structural sectional view showing a membrane type absolute humidity sensor according to the third embodiment of the present invention;

Figs. 8a and 8b are structural perspective views showing the

membrane type absolute humidity sensor according to the third embodiment of the present invention;

Figs. 9a and 9c are sectional views showing process steps of fabricating a silicon cap of the membrane type absolute humidity sensor according to the third embodiment of the present invention;

Figs. 10a and 10b are structural perspective views showing a package of the membrane type absolute humidity sensor according to the third embodiment of the present invention; and

Fig. 11 is a circuit diagram for detecting humidity based on the membrane type absolute humidity sensor according to the present invention.

Best Mode for Carrying Out the Invention

Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

First Embodiment

Fig. 2 is a structural sectional view showing a membrane type absolute humidity sensor according to the first embodiment of the present invention.

As shown in Fig. 2, a membrane 12 of SiO_2 , Si_3N_4 , SiO_xN_y , or $\text{SiO}_2/\text{Si}_3\text{N}_4/\text{SiO}_2$ is formed on a silicon substrate 11. A resistor material having a temperature coefficient of resistance (TCR) is deposited on the membrane 12 and then patterned to form a

resistor 13. The resistor is formed of one or more of Ti, Pt, Ni, Ni-Cr, and VO_2 .

Subsequently, a metal film is deposited on the resistor 13 having the TCR and then patterned to form an electrode pad 14 to be in contact with the resistor 13.

An insulating film 15 is formed on the resistor 13 to cover the resistor 13.

At this time, the insulating film 15 is formed of a material having excellent insulating characteristics, such as SiO_2 , Si_3N_4 , SiO_xN_y , a PSG, and polyimide.

Next, a metal film such as Al or Au is deposited on the insulating film 15 and then patterned to form a thermal conductive film 16 to be aligned with the resistor 13.

The thermal conductive film 16 acts to quickly emit heat of the resistor 13 to the outside. The thermal conductive film 16 may not be formed as the case may be.

Finally, a rear side of the substrate 11 is etched to expose a region of the membrane 12 where the resistor 13 is formed.

The membrane type absolute humidity sensor fabricated as above requires a temperature compensating element, which is not susceptible to variation of a peripheral humidity, and a humidity sensing element which detects variation of a peripheral humidity.

Fig. 3 is a structural sectional view showing a package of the membrane type absolute humidity sensor according to the first embodiment of the present invention.

As shown in Fig. 3, an absolute humidity sensor 21 for a humidity sensing element and an absolute humidity sensor 22 for a temperature compensating element are joined on a stem 23. Pins 24 are formed on the stem 23 to electrically connect with the outside. Electrode pads are formed in the absolute humidity sensors 21 and 22. The pins 24 are respectively connected with the electrode pads by wire.

Subsequently, a metal shield case 25 is sealed and joined over the stem to cover an entire surface of the stem 23 including the absolute humidity sensors 21 and 22.

A shielding film 25a is formed in a central region of the metal shield case 25 to separate and seal the absolute humidity sensor 21 for the humidity sensing element and the absolute humidity sensor 22 for the temperature compensating element.

A hole is formed in a region of the metal shield case 25, where the absolute humidity sensor for the humidity sensing element is formed, to pass through external humidity. A dry N₂ gas is filled in a region of the metal shield case 25, where the absolute humidity sensor for the temperature compensating element is formed, so as not to be affected by the external humidity.

Second Embodiment

In the second embodiment of the present invention, it is intended that an absolute humidity sensor for a humidity sensing element and an absolute humidity sensor for a temperature compensating element are integrated on one chip to facilitate

mass production.

Fig. 4 is a structural sectional view showing a membrane type absolute humidity sensor according to the second embodiment of the present invention.

As shown in Fig. 4, a membrane 32 of SiO_2 , Si_3N_4 , SiO_xN_y , or $\text{SiO}_2/\text{Si}_3\text{N}_4/\text{SiO}_2$ is formed on a silicon substrate 31. A resistor material having a TCR is deposited on the membrane 32 and then patterned to form a resistor 33 for a humidity sensing element and a resistor 33b for a temperature compensating element. The resistors 33a and 33b are formed of one or more Ti, Pt, Ni, Ni-Cr, and VO_2 .

Subsequently, a metal film is deposited on the resistors 33a and 33b having the TCR and then patterned to form an electrode pad 34 to be in contact with the resistors 33a and 33b.

An insulating film 35 is formed on the resistors 33a and 33b to cover entire surfaces of the resistors 33a and 33b.

At this time, the insulating film 35 is formed of a material having excellent insulating characteristics, such as SiO_2 , Si_3N_4 , SiO_xN_y , a PSG, and polyimide.

Also, a thermal conductive film of Al or Au may be formed on the insulating film 35, to be aligned with the resistors 33a and 33b, as the case may be.

Finally, a rear side of the substrate 31 is etched to expose a region of the membrane 32 where the resistors 33a and 33b are formed.

In the membrane type absolute humidity sensor fabricated as above, a temperature compensating element, which is not susceptible to variation of a peripheral humidity, and a humidity sensing element which detects variation of the peripheral humidity are simultaneously formed on one substrate. Accordingly, it is useful for mass production.

Fig. 5 is a structural sectional view showing a package of the membrane type absolute humidity sensor according to the second embodiment of the present invention, and Figs. 6a and 6b are structural perspective views showing the package of the membrane type absolute humidity sensor according to the second embodiment of the present invention.

As shown in Figs. 5, 6a and 6b, an absolute humidity sensor provided with a humidity sensing element 41 and a temperature compensating element 42 is joined on a stem 43. Pins 44 are formed on the stem 43 to electrically connect with the outside. Electrode pads are formed in the humidity sensing element 41 and the temperature compensating element 42. The pins 44 are respectively connected with the electrode pads by wire.

Subsequently, a metal shield case 45 is sealed and joined over the stem 43 to cover an entire surface of the stem 43 including the humidity sensing element 41 and the temperature compensating element 42.

Unlike the first embodiment, a shielding film is not formed in the metal shield case 45. A dry N₂ gas is filled in upper

portions of the humidity sensing element 41 and the temperature compensating element 42 so as not to be affected by the external humidity.

A hole is formed in only a region of the stem 45, where the humidity sensing element is formed, to pass external humidity through the membrane. A region, where the temperature compensating element 42 is formed, is sealed with the substrate 31 so as not to be affected by the external humidity.

Third Embodiment

The third embodiment of the present invention is identical to the second embodiment in its structure.

However, in the third embodiment of the present invention, external humidity is propagated into not a membrane through a stem formed at a lower portion but an insulating film through a shield case and a silicon cap which are formed at an upper portion.

Fig. 7 is a structural sectional view showing a membrane type absolute humidity sensor according to the third embodiment of the present invention, and Figs. 8a and 8b are structural perspective views showing the membrane type absolute humidity sensor according to the third embodiment of the present invention.

As shown in Figs. 7, 8a and 8b, a membrane 52 is formed on a silicon substrate 51. A resistor 53a for a humidity sensing element and a resistor 53b for a temperature compensating element

are formed on the membrane 52. The resistors 53a and 53b are formed of one or more of Ti, Pt, Ni, Ni-Cr, and VO₂.

Subsequently, an electrode pad 54 is formed to be in contact with the resistors 53a and 53b. An insulating film 55 is formed on the resistors 53a and 53b to cover entire surfaces of the resistors 53a and 53b.

At this time, to quickly emit heat the resistors 53a and 53b, as the case may be, a thermal conductive film of Al or Au may be formed on the insulating film 55, to be aligned with the resistors 53a and 53b.

A rear side of the substrate 51 is etched to expose a region of the membrane 52 where the resistors 53a and 53b are formed.

Finally, a silicon cap 56 is sealed and joined in an upper portion of the insulating film 55 to cover entire surfaces of a humidity sensing element 61 and a temperature compensating element 62.

A shielding film is formed in a central region of the silicon cap 56 to separate and seal the humidity sensing element 61 and the temperature compensating element 62.

A hole is formed in a region of the silicon cap 56, where the humidity sensing element 61 is formed, to pass through external humidity. A dry N₂ gas is filled in a region of the silicon cap 56, where the temperature compensating element is formed, so as not to be affected by the external humidity.

In the membrane type absolute humidity sensor fabricated as

above, the silicon cap covers the humidity sensing element and the temperature compensating element so that the external humidity is upwardly propagated.

The silicon cap according to the third embodiment of the present invention can be fabricated by simple process steps.

Figs. 9a and 9c are sectional views showing process steps of fabricating the silicon cap of the membrane type absolute humidity sensor according to the third embodiment of the present invention.

As shown in Fig. 9a, first and second etching mask films 71 and 72 are formed at both sides of a silicon substrate 70. The first and second etching mask films 71 and 72 are formed of Si_3N_4 or CrN.

Subsequently, as shown in Fig. 9b, a predetermined region of the first etching mask 71 is removed to expose the silicon substrate 70. The exposed silicon substrate 70 is then etched at a predetermined depth by a wet or dry etching method.

As shown in Fig. 9c, a predetermined region of the second etching mask 72 is removed to expose the silicon substrate 70. The exposed silicon substrate 70 is then etched by the wet or dry etching method to form first and second recess regions. At this time, the silicon substrate 70 is etched at a depth joined with a lower portion of the silicon substrate etched in the step of Fig. 9b.

In the silicon cap of the present invention fabricated as

above, the first and second recesses separated from each other by the silicon etching are formed, a hole is formed in a bottom of the first recess to pass through external humidity, and no hole is formed in a bottom of the second recess so as not to pass through the external humidity.

Figs. 10a and 10b are structural perspective views showing a package of the membrane type absolute humidity sensor according to the third embodiment of the present invention.

As shown in Figs. 10 and 10b, an absolute humidity sensor provided with a humidity sensing element 61 and a temperature compensating element 62 is joined on a stem 63. Pins 64 are formed on the stem 63 to electrically connect with the outside. Electrode pads are formed in the humidity sensing element 61 and the temperature compensating element 62. The pins 64 are respectively connected with the electrode pads by wire.

Subsequently, a metal shield case 65 is sealed and joined over the stem 63 to cover an entire surface of the stem 63 including the silicon cap 56.

Unlike the first embodiment, a shielding film is not formed in the metal shield case 65. A hole is formed in the metal shield case 65 to pass through the external humidity. That is, the external humidity is propagated into a region, where the humidity sensing element 61 is formed, through the hole of the metal shield case 65 and the hole of the silicon cap 56.

Fig. 11 is a circuit diagram for detecting the humidity

based on the membrane type absolute humidity sensor according to the present invention.

Referring to Fig. 11, the circuit for detecting variation of the peripheral humidity using the membrane type absolute humidity sensor according to the present invention includes a bridge circuit and a power source (V) applied to the bridge circuit. The bridge circuit consists of a humidity sensing element 61, a temperature compensating element 62, a fixed resistor R1, and a variable resistor VR.

As an example, a method for detecting variation of the humidity by water vapor generated from food during cooking of food in a microwave oven using the absolute humidity sensor and the above circuit will be described below.

First, if the food is heated in the microwave oven, the water vapor is generated. The generated water vapor is propagated into the sensor through a hole formed inside the sensor. Thus, the water vapor is in contact with the humidity sensing element 61.

At this time, a resistor of the humidity sensing element 61 is self-heated by a bias voltage. Accordingly, the water vapor contacted with the humidity sensing element 61 absorbs heat of the resistor. For this reason, the resistor of the humidity sensing element 61 has a reduced temperature by heat loss, thereby varying a resistance value.

However, since the temperature compensating element 62 does

not contact with the water vapor, variation of a resistance value does not occur.

The resistance variation of the humidity sensing element 61 causes output variation of the bridge circuit, thereby detecting the humidity variation.

Accordingly, the humidity variation around the sensor can easily be detected by the absolute humidity sensor and the above circuit. The water vapor generated from the food due to heat during cooking of food in a cooking machine such as a microwave oven is detected to apply for automatic cooking of food.

Industrial Applicability

As aforementioned, the membrane type absolute humidity sensor according to the present invention has the following advantages.

Since the resistor having the TCR is formed, it is possible to improve sensitivity of the sensor and obtain high response time. Furthermore, since the sensor is integrated in one chip using a silicon micro-machining process, a compact size and simple process steps can be achieved to facilitate mass production. Also, the production cost can be saved.

The foregoing embodiments are merely exemplary and are not to be construed as limiting the present invention. The present teachings can be readily applied to other types of apparatuses. The description of the present invention is intended to be

illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will be apparent to those skilled in the art.

What is claimed is:

1. An absolute humidity sensor comprising:
 - a substrate having a cavity;
 - a membrane formed on the substrate;
 - a resistor formed on the membrane;
 - electrode pads formed on the membrane, for electrically connecting with the resistor; and
 - a passivation film formed on an entire surface of the resistor to cover the resistor.
2. The absolute humidity sensor of claim 1, wherein the membrane is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , and $\text{SiO}_2/\text{Si}_3\text{N}_4/\text{SiO}_2$.
3. The absolute humidity sensor of claim 1, wherein the resistor is formed of one or more of Ti, Pt, Ni, Ni-Cr, and VO_2 .
4. The absolute humidity sensor of claim 1, wherein the passivation film is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , phosphor silicate glass (PSG), and polyimide.
5. The absolute humidity sensor of claim 1, further comprising a thermal conductive film formed on a region of the passivation film where the resistor is formed.

6. The absolute humidity sensor of claim 5, wherein the thermal conductive film is formed on any one of Al and Au.

7. An absolute humidity sensor comprising:
a substrate having a first cavity and a second cavity in a predetermined region;
a membrane formed on the substrate;
a humidity sensing element formed on the membrane where the first cavity is formed, for detecting humidity exposed to the air, having a variable resistance value depending on the detected humidity; and
a temperature compensating element formed on the membrane where the second cavity is formed, for compensating for the resistance value of the humidity sensing element.

8. The absolute humidity sensor of claim 7, wherein the humidity sensing element and the temperature compensating element include:

a resistor formed on the membrane;
electrode pads formed on the membrane, for electrically connecting with the resistor; and
a passivation film formed on an entire surface of the resistor to cover the resistor.

9. The absolute humidity sensor of claim 8, wherein the

membrane is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , and $\text{SiO}_2/\text{Si}_3\text{N}_4/\text{SiO}_2$.

10. The absolute humidity sensor of claim 8, wherein the resistor is formed of one or more of Ti, Pt, Ni, Ni-Cr, and VO_2 .

11. The absolute humidity sensor of claim 8, wherein the passivation film is formed of any one of SiO_2 , Si_3N_4 , SiO_xN_y , phosphor silicate glass (PSG), and polyimide.

12. The absolute humidity sensor of claim 7, further comprising a thermal conductive film formed on a region of the passivation film where the resistor is formed.

13. The absolute humidity sensor of claim 12, the thermal conductive film is formed on any one of Al and Au.

14. The absolute humidity sensor of claim 7, further comprising a cap formed over the humidity sensing element and the temperature compensating element to cover the entire surfaces of the humidity sensing element and the temperature compensating element, for separating the humidity sensing element and the temperature compensating element from each other and sealing them therein.

15. The absolute humidity sensor of claim 14, a shielding film is formed in a central region of the cap to separate and seal the humidity sensing element and the temperature compensating element.

16. The absolute humidity sensor of claim 14, a hole is formed in a region of the cap, where the humidity sensing element is formed, to pass through external humidity.

17. The absolute humidity sensor of claim 14, wherein the cap is made of silicon.

18. The absolute humidity sensor of claim 7, further comprising:

a stem joined with a lower portion of the substrate, having pins for electrically connecting with the outside;

a wire for electrically connecting the electrode pads of the humidity sensing element and the temperature compensating element with the pins of the stem; and

a metal shield case formed on an upper portion of the stem to cover an entire surface of the stem including the humidity sensing element and the temperature compensating element.

19. The absolute humidity sensor of claim 18, a hole is formed in a region of the stem, where the humidity sensing

element is formed, to pass through external humidity.

20. The absolute humidity sensor of claim 18, a hole is formed in the shield case to pass through external humidity.